TOSHIBA GTR Module Silicon N Channel IGBT

MG300Q2YS50

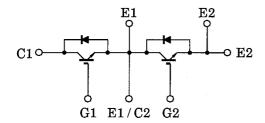
High Power Switching Applications Motor Control Applications

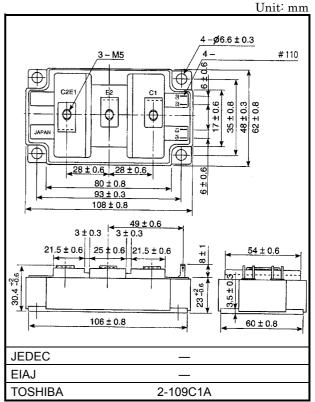
- High input impedance
- High speed : $t_f = 0.3\mu s$ (Max.) Inductive load
- Low saturation voltage

 $V_{CE (sat)} = 3.6 V (Max.)$

- Enhancement-mode
- Includes a complete half bridge in one package.
- The electrodes are isolated from case.

Equivalent Circuit





Weight: 430g

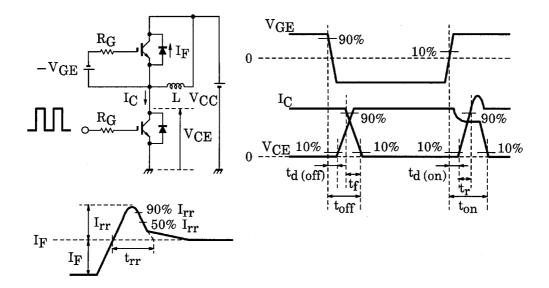
Maximum Ratings (Ta = 25°C)

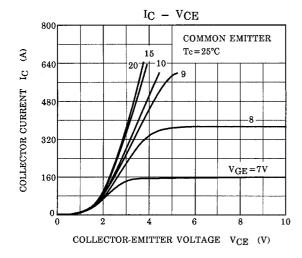
Characteristic		Symbol	Rating	Unit	
Collector-emitter voltage		V _{CES}	1200	V	
Gate-emitter voltage		V_{GES}	±20	V	
Collector current	DC	I _C (25°C / 80°C)	400 / 300	A	
	1ms	I _{CP} (25°C / 80°C) 800 / 600		Α	
Forward current	DC	IF	300	Α	
	1ms	I _{FM}	600		
Collector power dissipation (Tc = 25°C)		PC	2000	W	
Junction temperature		Tj	150	°C	
Storage temperature range		T _{stg}	- 40 ~ 125	°C	
Isolation voltage		V _{Isol}	2500 (AC 1 minute)	V	
Screw torque (Terminal / mounting)		_	3/3	N·m	

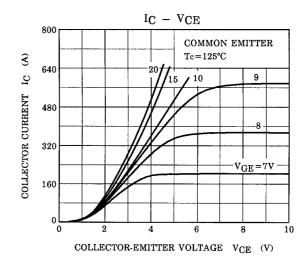
Electrical Characteristics (Ta = 25°C)

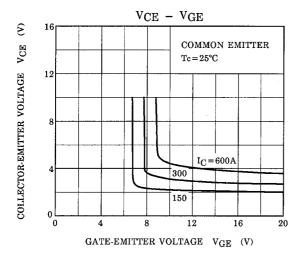
Cha	Characteristic Symbol Test Condition		on	Min	Тур.	Max	Unit	
Gate leakage current		I _{GES}	V _{GE} = ±20V, V _{CE} = 0		_	_	±500	nA
Collector cut-off current		I _{CES}	V _{CE} = 1200V, V _{GE} = 0		_	_	1.0	mA
Gate-emitter cut-o	ff voltage	V _{GE (OFF)} I _C = 300mA, V _{CE} = 5V			3.0	_	6.0	V
Collector-emitter saturation voltage		V _{CE} (sat)	I _C = 300A, V _{GE} = 15V	T _j = 25°C	_	2.8	3.6	V
				T _j = 125°C	_	3.1	4.0	
Input capacitance		C _{ies}	V _{CE} = 10V, V _{GE} = 0, f = 1MHz		_	30.0	_	nF
Switching time	Turn-on delay time	t _{d (on)}			_	0.05	_	
	Rise time	t _r	Inductive load V _{CC} = 600V I _C = 300A		_	0.05	_	μs
	Turn-on time	t _{on}			_	0.2	_	
	Turn-off delay time	t _{d (off)}	$V_{GE} = \pm 15V$ $R_G = 2.7\Omega$		_	0.5	_	
	Fall time	t _f	11.0 2.7.12	(Note 1)	_	0.1	0.3	
	Turn-off time	t _{off}			_	0.6	_	
Forward voltage		V _F	I _F = 300 A, V _{GE} = 0		_	2.4	3.5	V
Reverse recovery time		t _{rr}	$I_F = 300 \text{ A}, V_{GE} = -10 \text{ V},$ di / dt = 1000 A / μ s (Note 1)		_	0.2	0.3	μs
Thermal resistance		R _{th (j-c)}	Transistor stage		_	_	0.06	°C/W
			Diode stage		_	_	0.19	

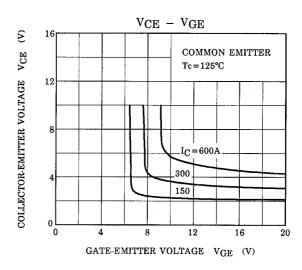
Note 1: Switching time and reverse recovery time test circuit & timing chart

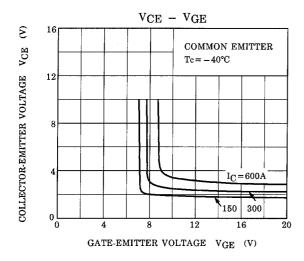


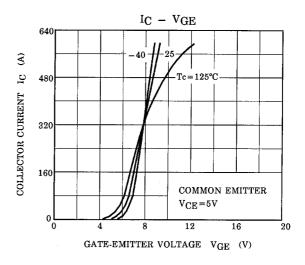


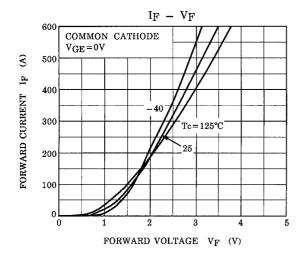


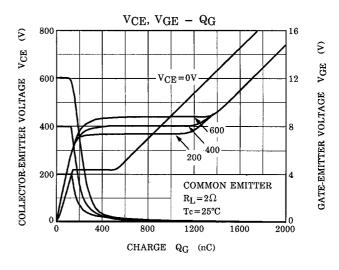


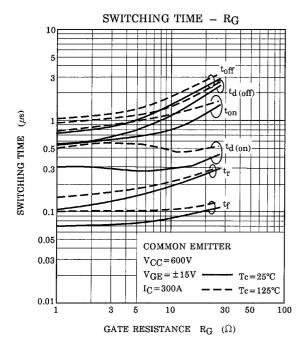


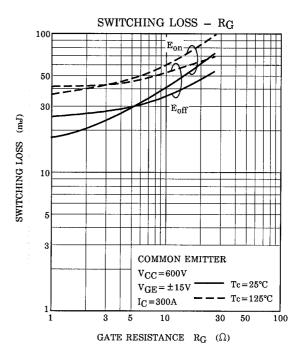


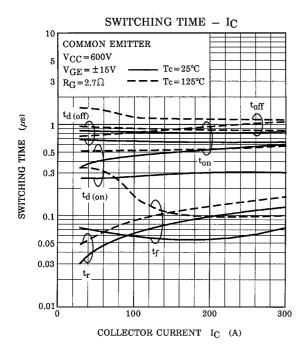


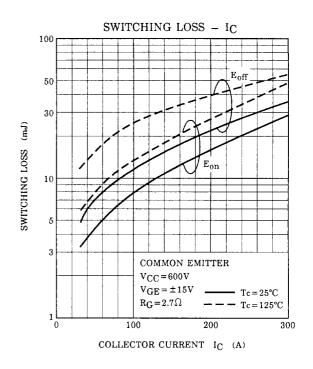


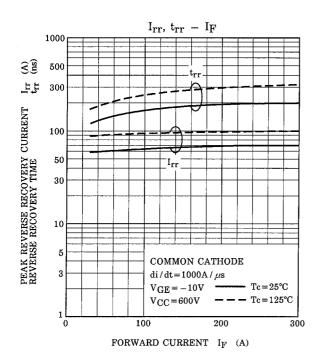


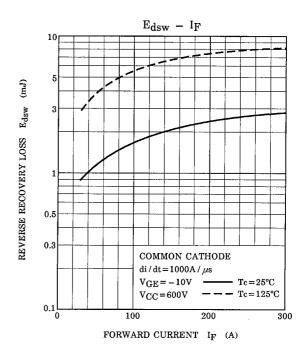




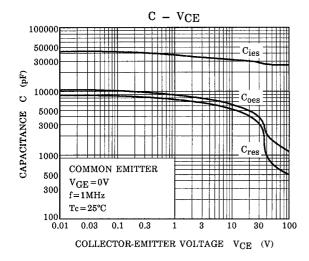


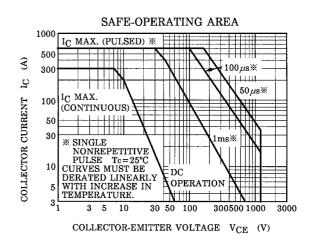


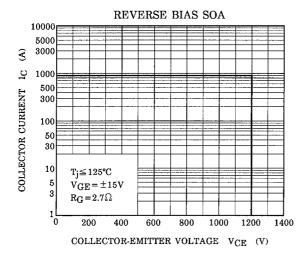


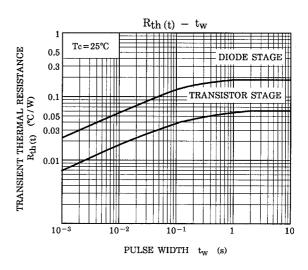


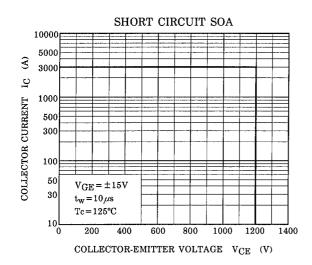
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